

Gigabit 4-Phase Modulation-Demodulation Circuits for Microwave Digital Systems

K. Miyauchi, S. Seki and K. Yanagimoto. "Gigabit 4-Phase Modulation-Demodulation Circuits for Microwave Digital Systems." 1979 MTT-S International Microwave Symposium Digest 79.1 (1979 [MWSYM]): 531-533.

An extremely high speed modulation-demodulation performance is discussed when data-rate becomes close to carrier frequency. A 4-phase PSK modulation-demodulation circuit with a newly designed transformer is described. The error-rate performance on 1.6 Gb/s data-rate was successfully undertaken at a 1.7 GHz carrier frequency.

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